
Power Management Intergrated Circuit with 4 Bucks and 2LDOs

1 Description

The SY5881 is a power management IC (PMIC) designed for notebook, desktop, and embedded computing platforms. There are four DCDC converters and two linear regulators integrated into the SY5881, which can minimize the need for external components so that it can ease the layout in a constrained PCB area. The DCDC converters are of high accuracy, high efficiency, and high flexibility. Two LDOs provide power rails for peripherals and pull-up resistors.

The SY5881 provides protection for a device when a fault occurs, such as overcurrent, output overvoltage, output undervoltage, short circuit, input overvoltage, and over temperature.

The SY5881 is a halogen- and lead-free device, compliant with RoHS. The package is WLCSP-36 2.4x2.4 .

2 Applications

Solid-state drive

Video system

3 Features

- AVIN input supply range: 2.7V to 3.7V
- Four buck regulators
 - BUCK1,output range 1.7V ~ 2.9V
 - BUCK2,output range 0.9V ~ 2.0V
 - BUCK3,output range 0.5V ~ 1.3V
 - BUCK4,output range 0.675V ~ 2.0V
- 2 linear regulators
 - LDO1, 1V ~ 2.7V, 50mV/Step, 0.4A
 - LDO2, 1V ~ 2.7V, 50mV/Step, 0.4A
- 2 external converters
- DCDC voltage accuracy:±1% FB Voltage
- PWM frequency adjustable:
1Mhz~3Mhz, step 500Khz
- BUCK1 can be set as load switch
- BUCK4 can be set as linear regulator
- LDO1/LDO2 can be set as load switch
- Configurable Soft-start Time for All output
- Configurable power on sequence
- Configurable sleep and wake-up sequence
- I2C Interface 400kHz/1MHz/3.4MHz
- Built-in AVIN OV/UV warning indication and fault protection
- Built-in VOUT OVP/UVP/OCL
- Built-in Overd Temperature Protection
- Built-in EFUSE for configurable default value of registers
- 8 multifunctional IOs

4 Typical Application Circuit

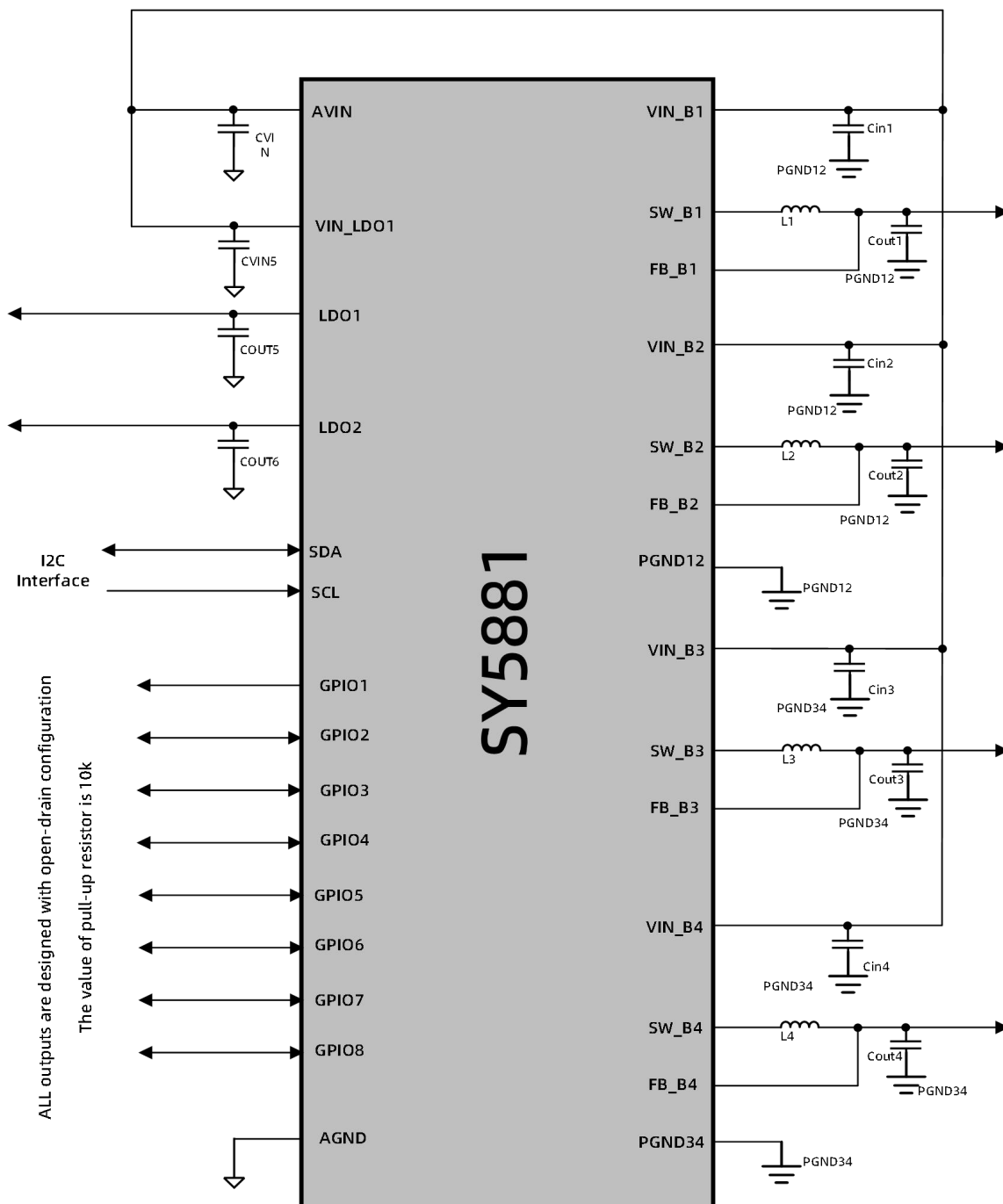


Figure 4- 1. Typical Application Circuit